

PATENT APPLICATION
Q77404

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

In re application of

Keiji HAYASHIDA, et al.

Appln. No.: 10/664,884

Group Art Unit: 2817

Confirmation No.: 4264

Examiner: Arnold M. Kinhead

Filed: September 22, 2003

For: PHASE-LOCKED LOOP CIRCUIT REDUCING STEADY STATE PHASE ERROR

EXCESS CLAIM FEE PAYMENT LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

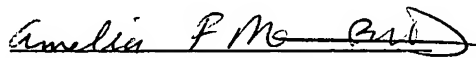
An Amendment Under 37 C.F.R. § 1.111 is attached hereto for concurrent filing in the above-identified application. The resulting excess claim fee is calculated as shown below:

	After Amendment		Highest No. Previously Paid For		No. Extra		Rate		Fee Due
All Claims	18	-	20	=	0	X	\$50.00	=	0
Independent	9	-	3	=	6	X	\$200.00	=	\$1,200.00
TOTAL FEE DUE									= \$1,200.00

A check for the statutory fee of \$1,200.00 is attached. The USPTO is directed and authorized to charge all additional required fees (except the Issue/Publication Fees) to our Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this letter is also enclosed.

Respectfully submitted,

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860


Amelia F. Morani, Ph.D.
Registration No. 52,049

WASHINGTON OFFICE

23373

CUSTOMER NUMBER

Date: June 8, 2005



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AMENDMENT UNDER 37 C.F.R. §1.111

MAIL STOP AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated March 8, 2005, please amend the above-identified application as follows on the accompanying pages.

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06/09/2005 SDENB081 00000082 10664884

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